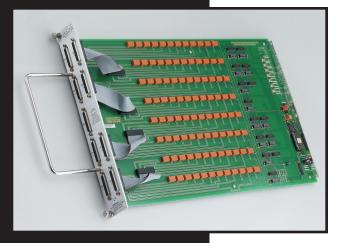
7075

Two-Pole Multiplexer Card



The Model 7075 is a general purpose multiplex switching card that consists of eight banks of independent 1×12 multiplexer switching. Each bank has two switched circuits (HI and GUARD). The row is connected through jumpers on the card to the general purpose analog backplane in the Model 707A or 708A switching mainframe. This provides the interconnect between cards for multiplexer expansion (1×24 , 1×36 , etc.). Jumpers may be removed to isolate any bank. A single card can be expanded to 1×96 by reconfiguring the supplied bank-to-bank jumpers. Eight 25-pin D connectors are provided for bank connections and one for row connection.

- Low cost
- <5µV voltage offset
- <100pA offset current</p>
- 30MHz bandwidth
- 110V, 1A signal levels
- Uses standard 25-pin D connectors

Ordering Information

7075 Eight 1x12 Two-Pole Multiplexer Card

Extended warranty, service, and calibration contracts are available.

Accessories Supplie

Jumpers for multiplexer expansion.

ACCESSORIES AVAILABLE

7076-RMTC	High Isolation Row Cable Assembly, 3m (10 ft)
7076-CMTC	High Isolation Column/Bank Cable Assembly, 3m (10 ft)
7075-MTC	Row/Column/Bank Standard Cable Assembly, 3m (10 ft)

- MULTIPLEX CONFIGURATION: Eight 1×12 banks. Adjacent banks can be connected together. Jumpers can be removed to isolate any bank from the backplane.
- CONTACT CONFIGURATION: 2-pole Form A (HI, GUARD).
- **CONNECTOR TYPE:** 25-pin subminiature D connector, eight for bank connection, one for row connection.

MAXIMUM SIGNAL LEVEL:

- DC Signals: 110V DC pin-to-pin, 1A switched, 30VA (resistive load).
- AC Signals: 175V AC peak pin-to-pin, 1A switched, 60VA (resistive load).
- **COMMON MODE VOLTAGE:** 110V DC, 175V AC peak pin-to-pin or pin-to-chassis.

CONTACT LIFE:

- Cold Switching: 108 closures.
- At Maximum Signal Level: 105 closures.
- CHANNEL RESISTANCE (per conductor): $<0.50\Omega$ initial, $<1.5\Omega$ at end of contact life.

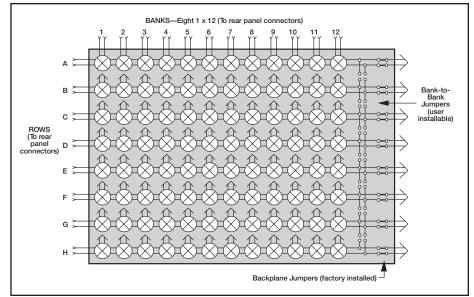
CONTACT POTENTIAL: <5µV per contact pair (HI to GUARD). **OFFSET CURRENT:** <100pA.

CROSSTALK (1MHz, 50Ω load): Bank: <-60dB. Channel: <-60dB.</p>

INSERTION LOSS (1MHz, 50Ω source, 50Ω load): 0.1dB typical.

ISOLATION:

- Bank: >10¹⁰ Ω , <3pF.
- **Channel**: >10¹⁰ Ω , <5pE
- **Differential**: Configured as 1×12 : $> 10^{9}\Omega$, < 100pF nominal. Configured as 1×96 : $> 10^{8}\Omega$, < 600pF nominal.
- **Common Mode**: Configured as 1×12: >10⁹Ω, <165pF nominal. Configured as 1×96: >10⁸Ω, <700pF nominal.
- 3dB BANDWIDTH (50Ω load):
 - Configured as 1×12: 30MHz typical.
 - Configured as 1×96: 2.5MHz typical.
- RELAY DRIVE CURRENT (per relay): 28mA.
- RELAY SETTLING TIME: <3ms.





CONTROL

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SWITCHING

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